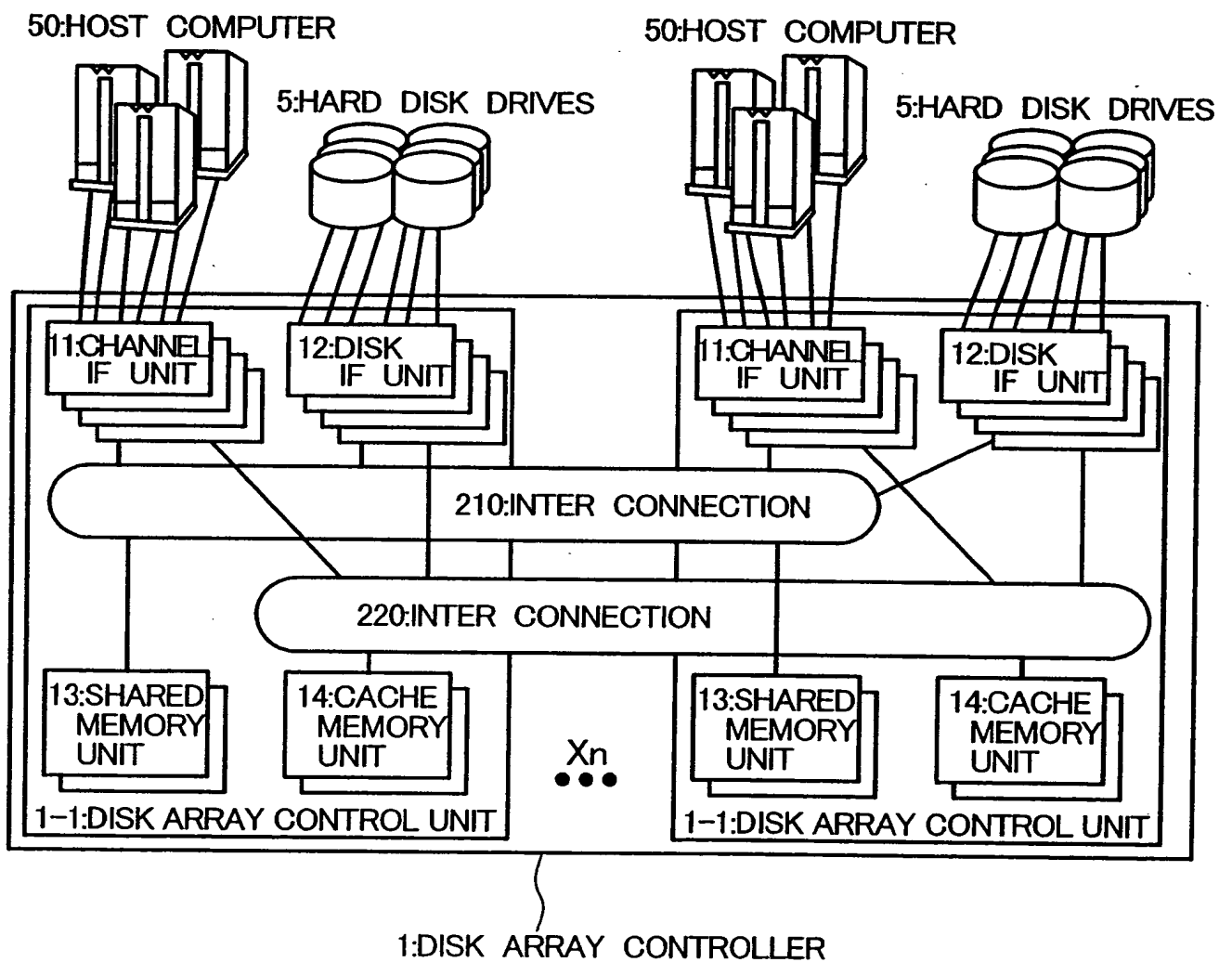
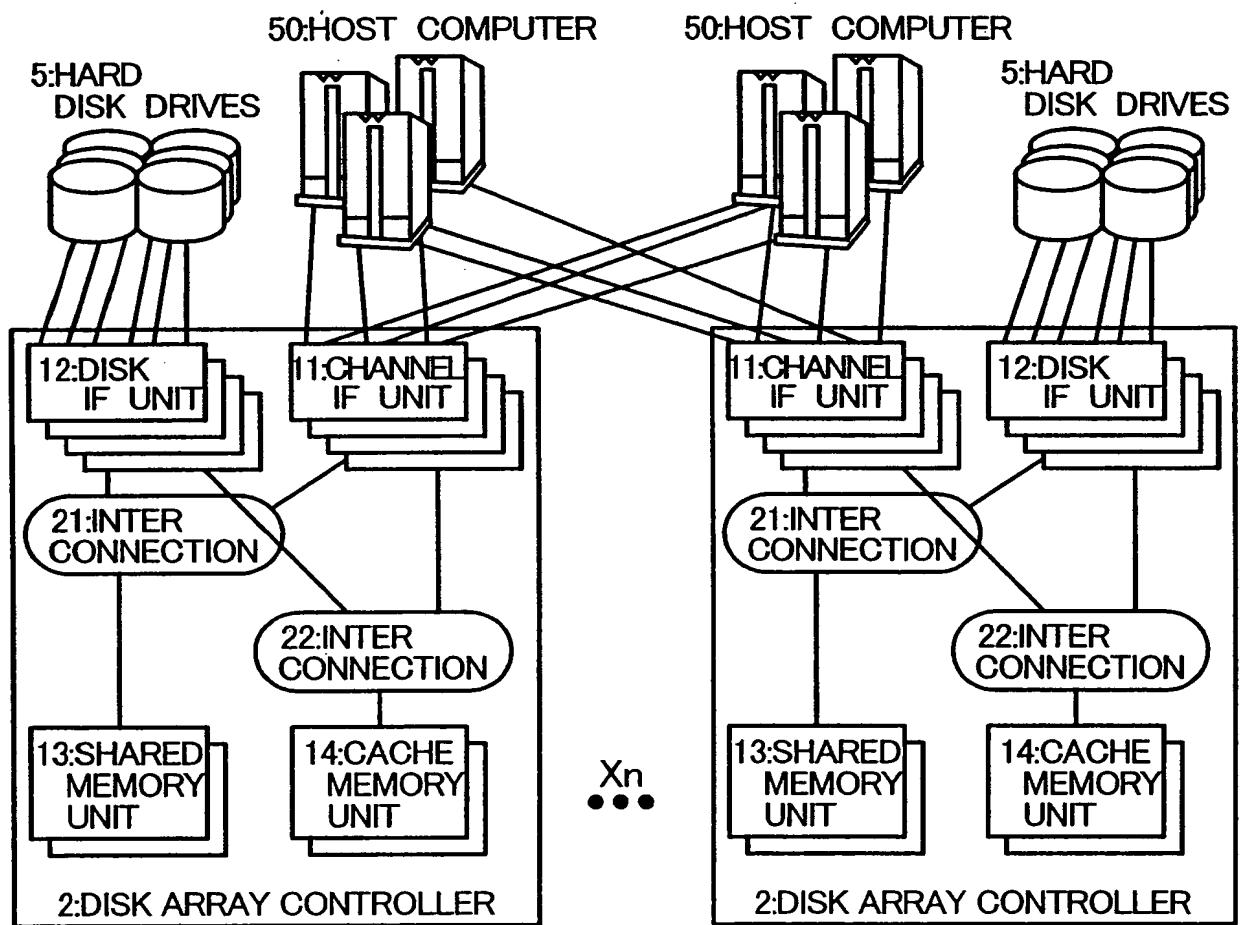


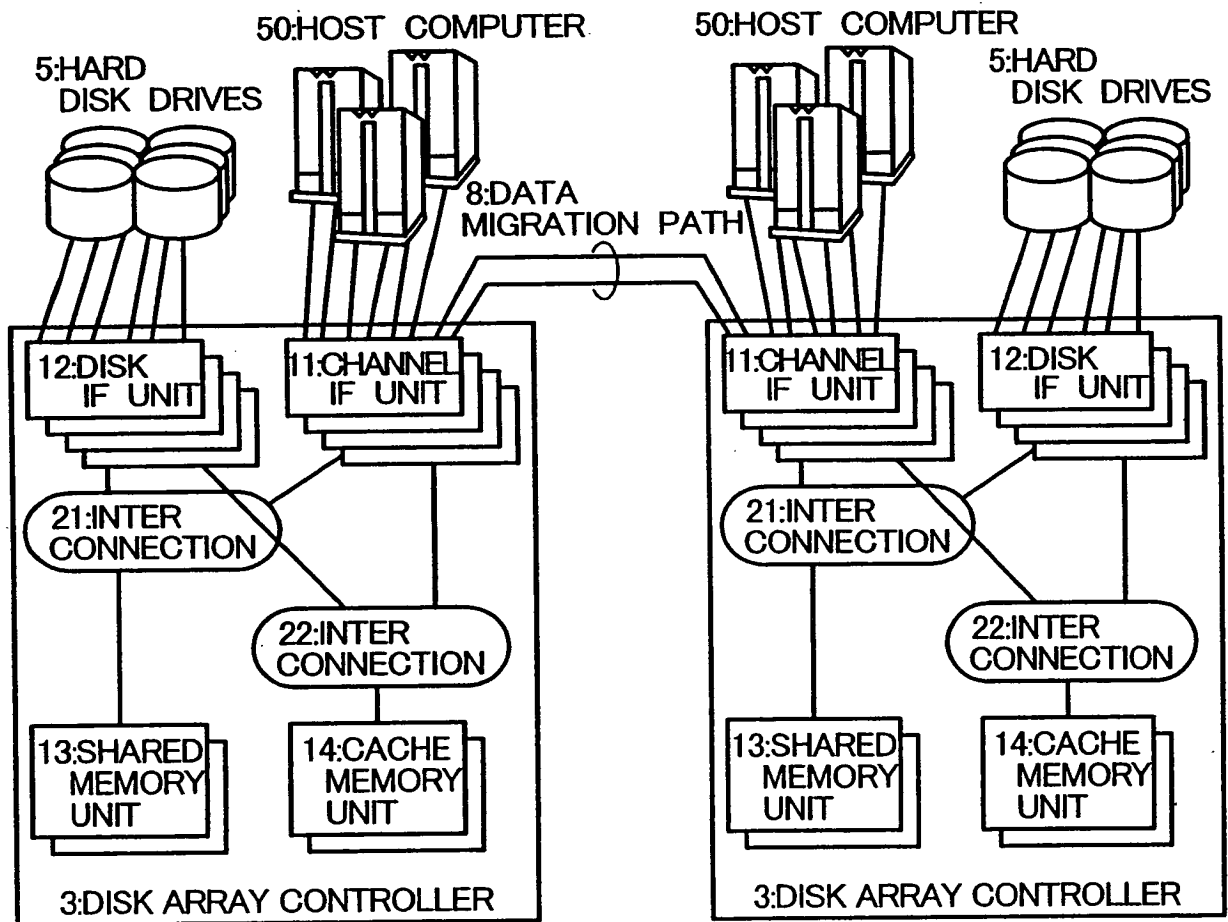
FIG.1



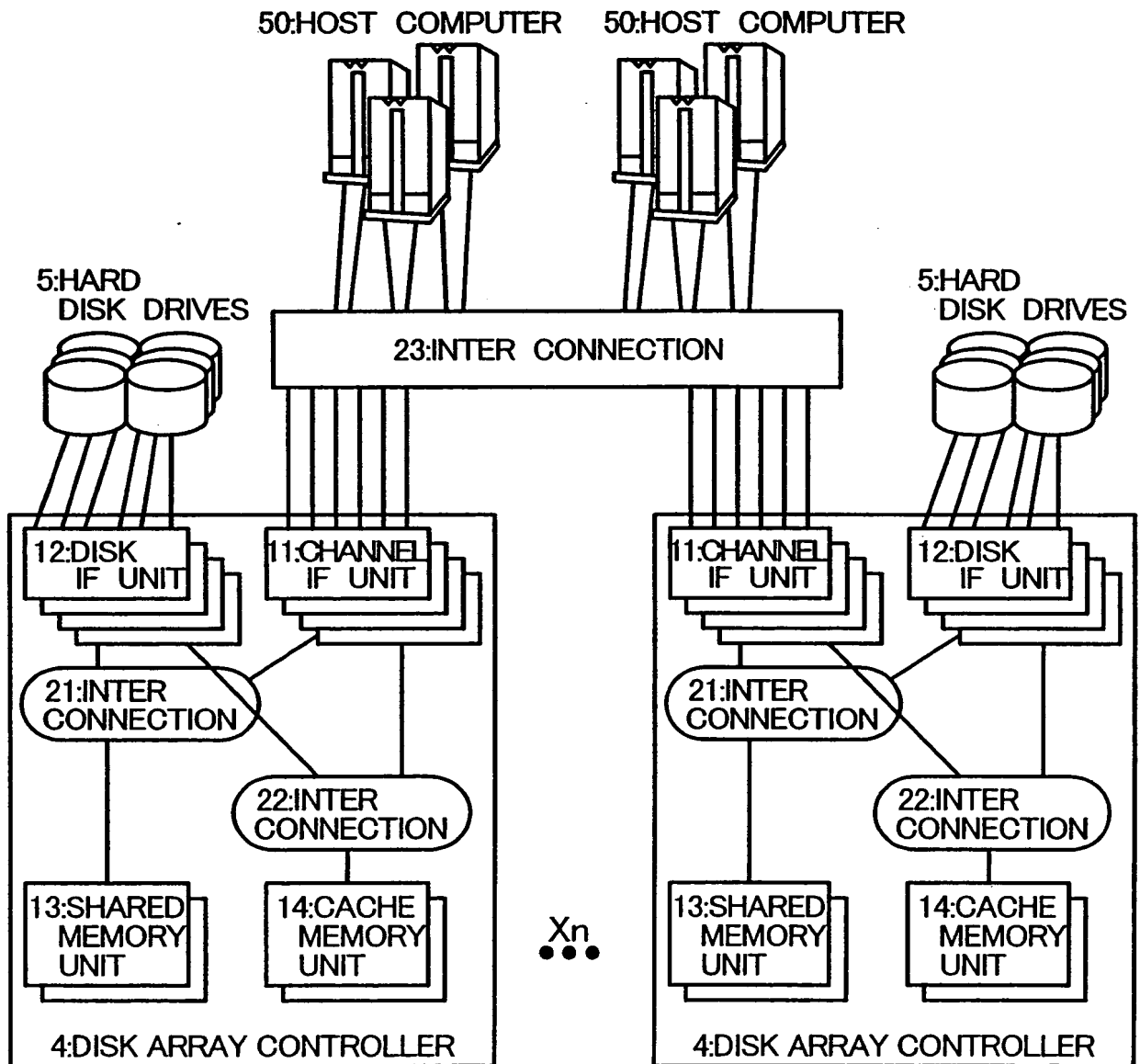
**FIG.2**  
(PRIOR ART)



**FIG.3**  
(PRIOR ART)



**FIG.4**  
(PRIOR ART)



# FIG.5

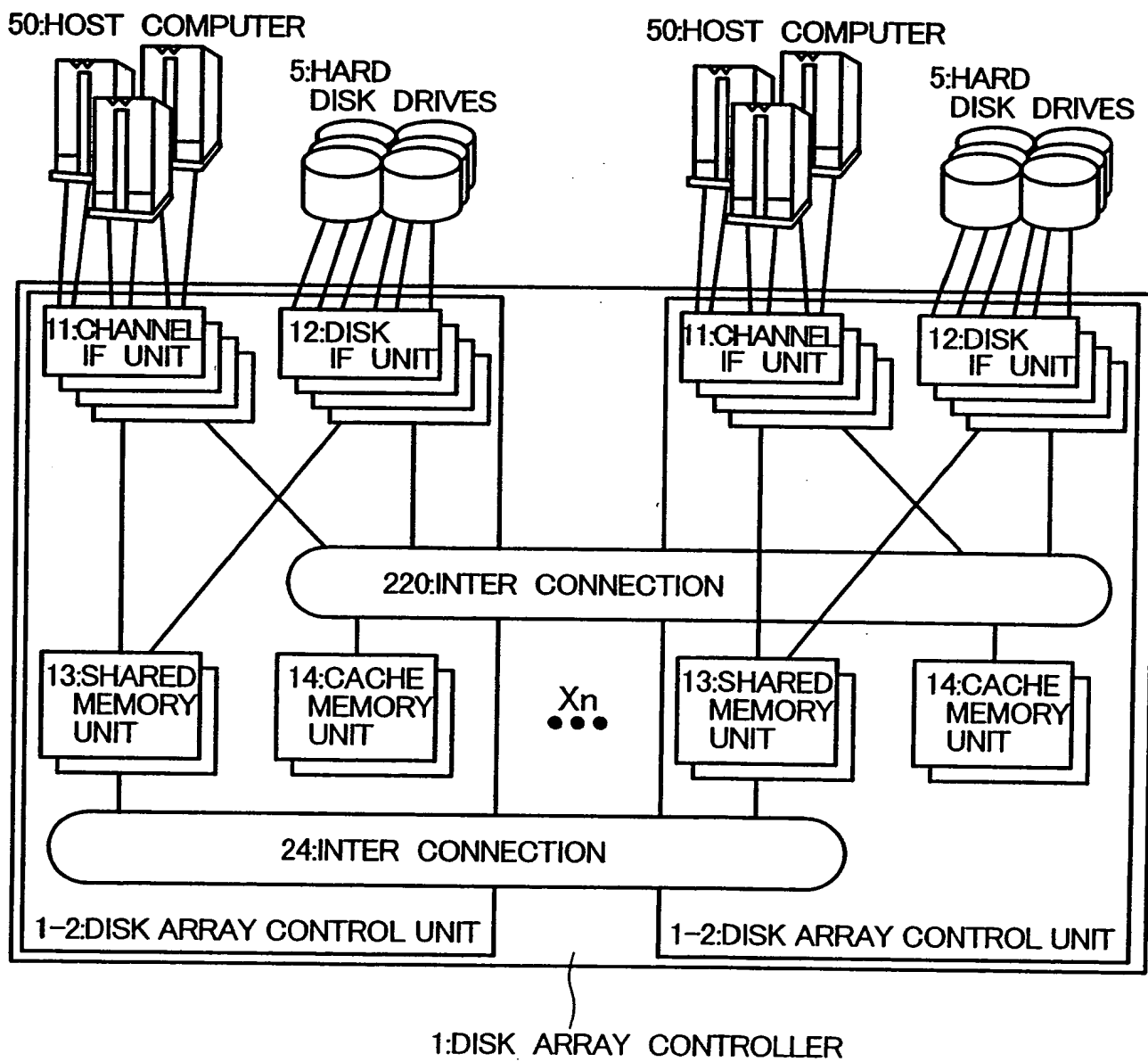
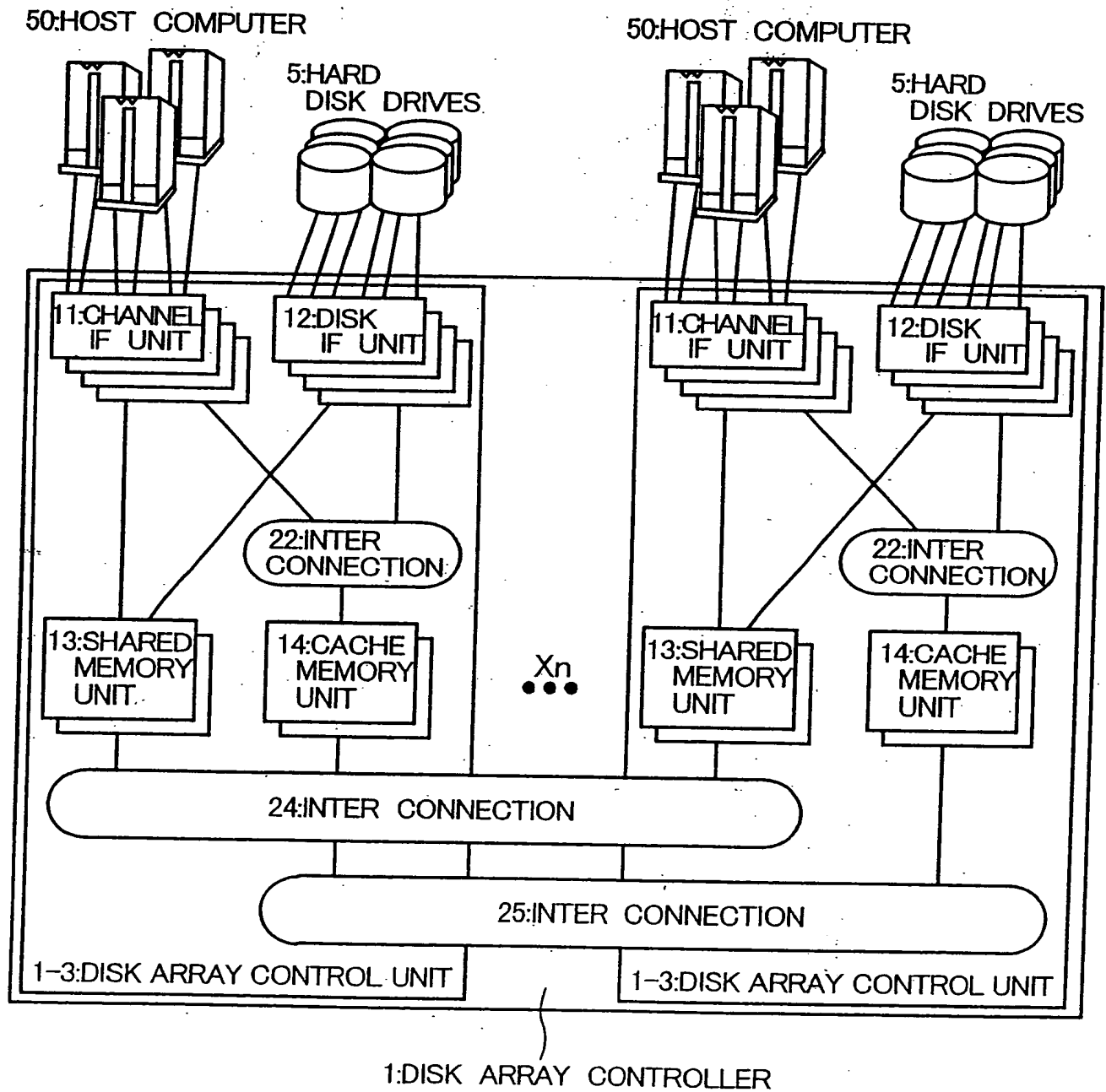
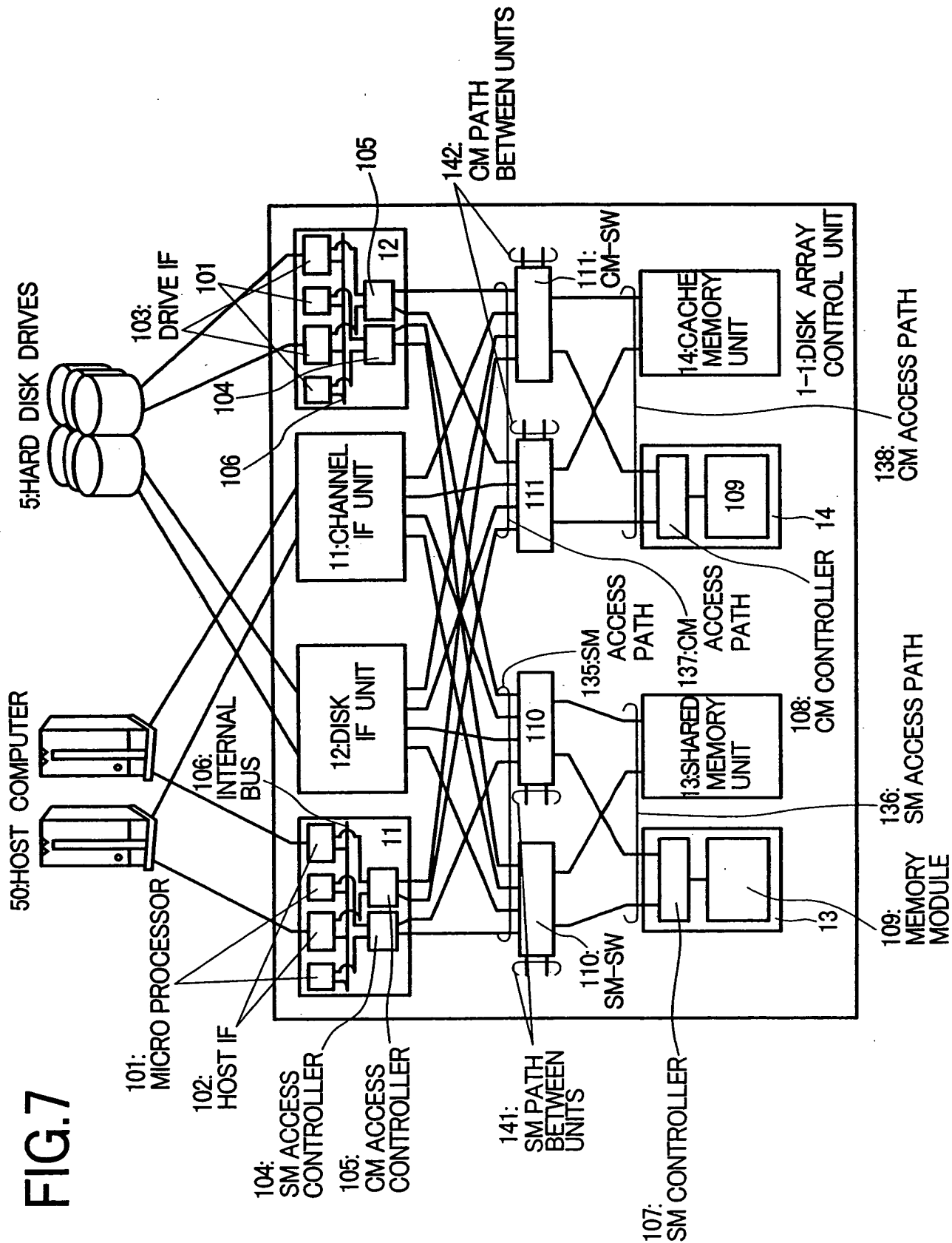


FIG.6



# FIG. 7



# FIG.8

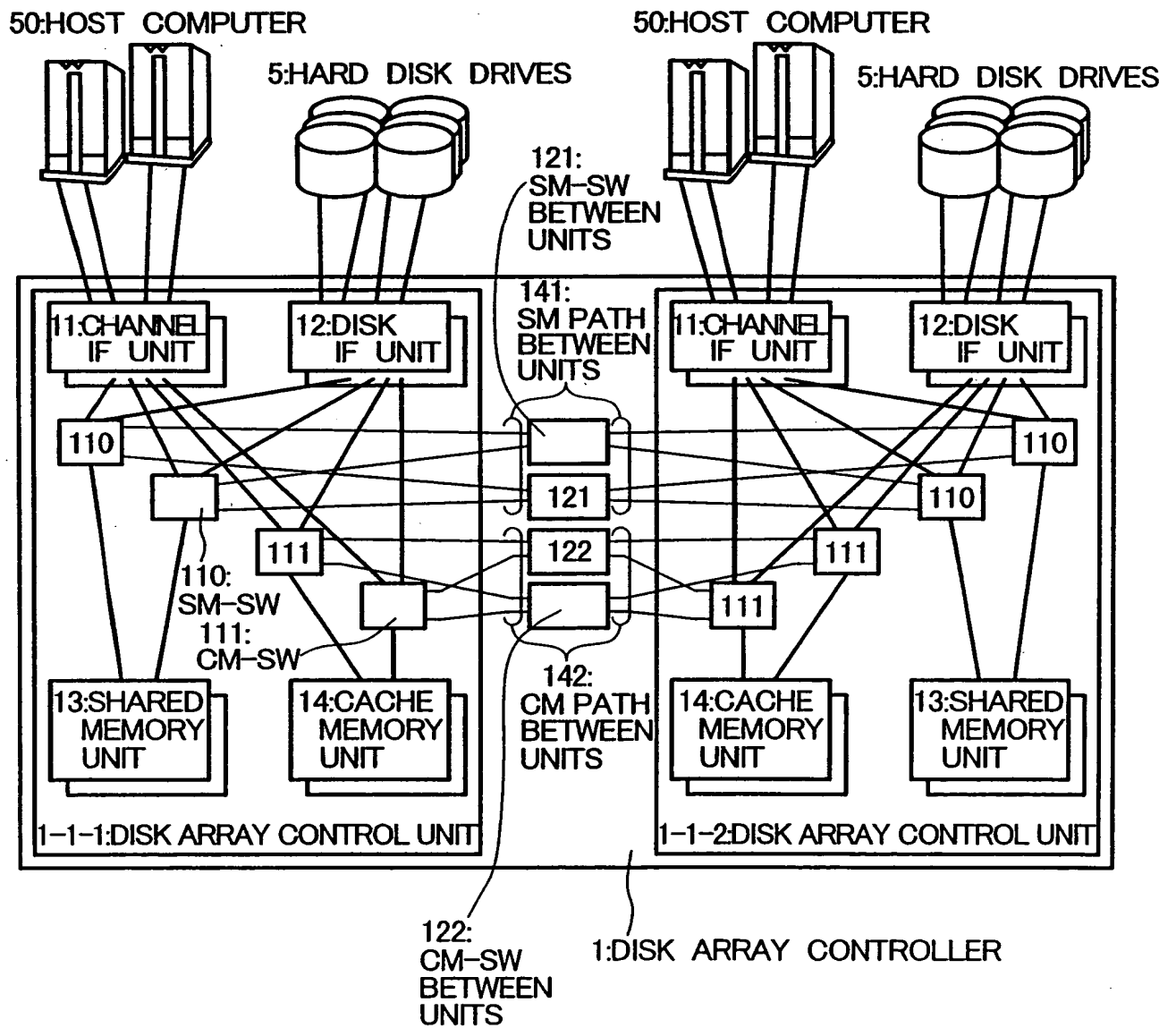
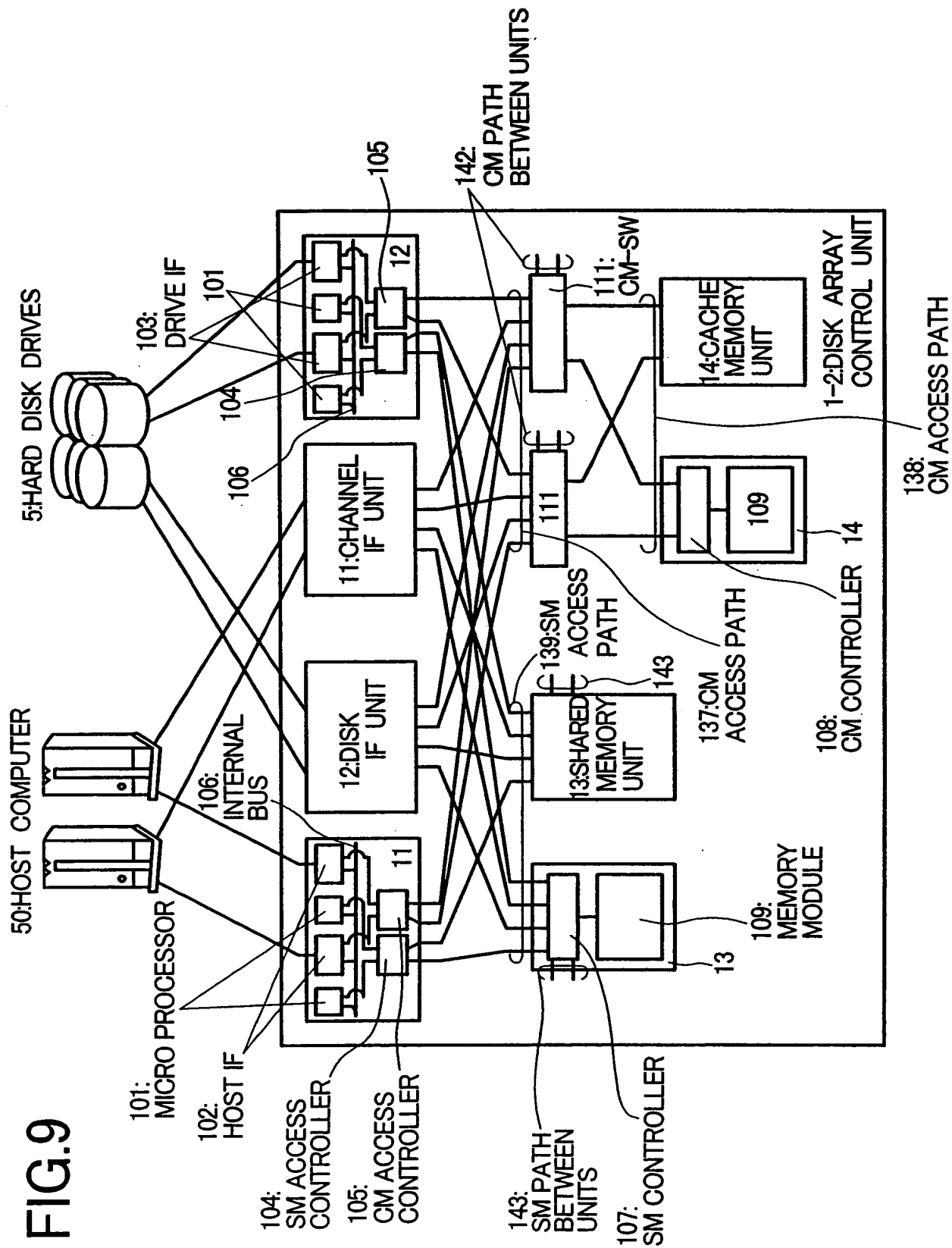


FIG.9



# FIG.10

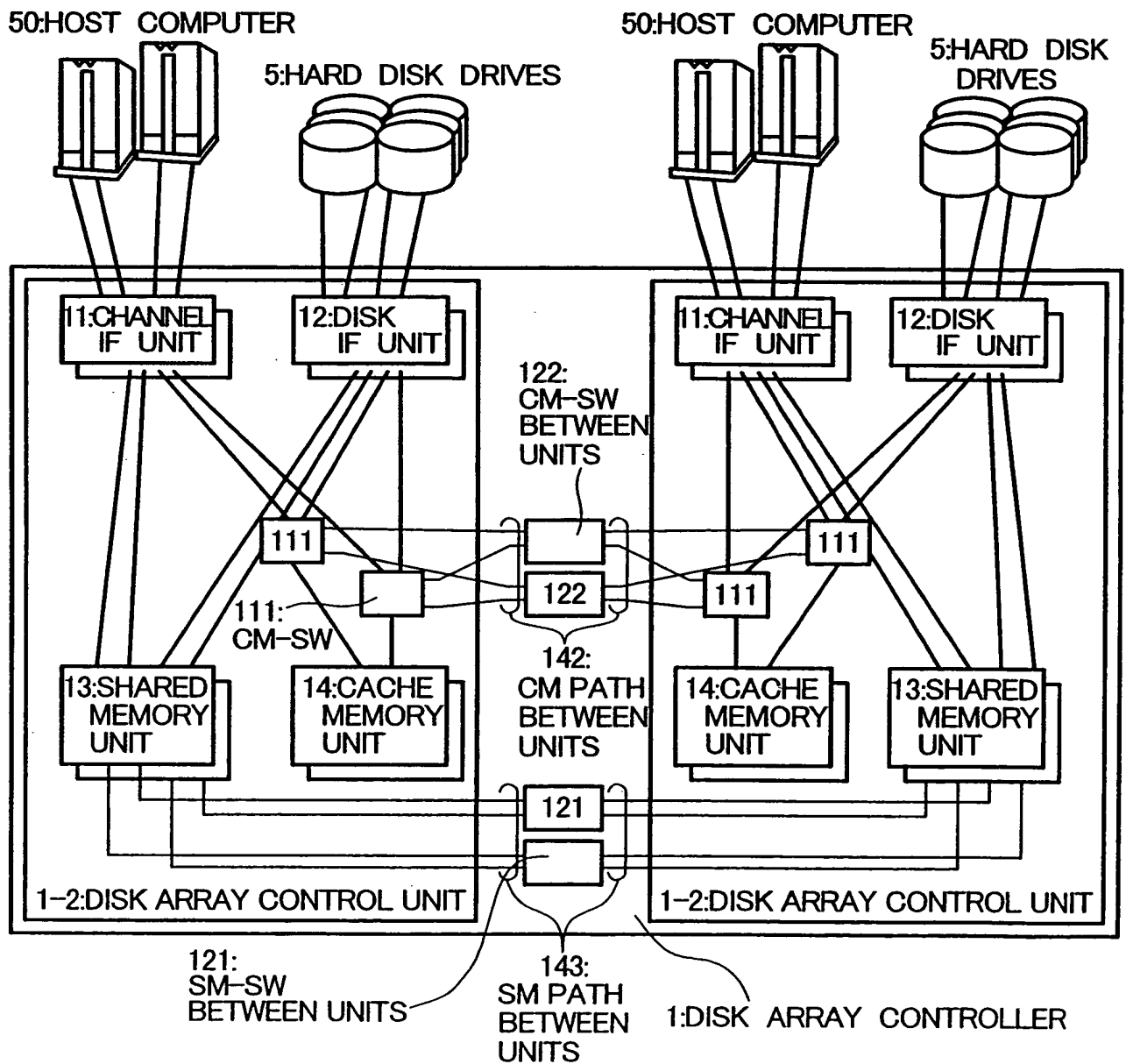
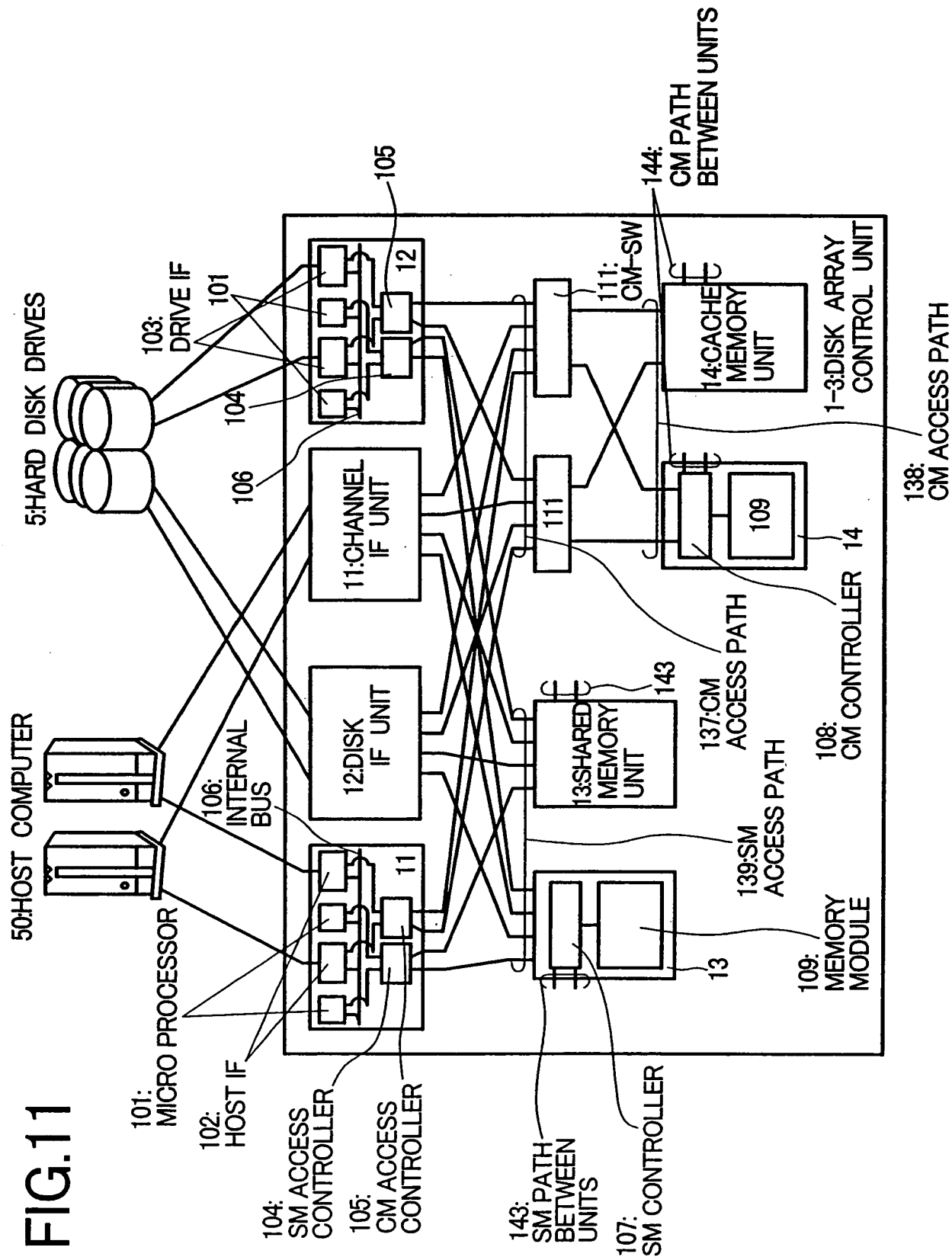
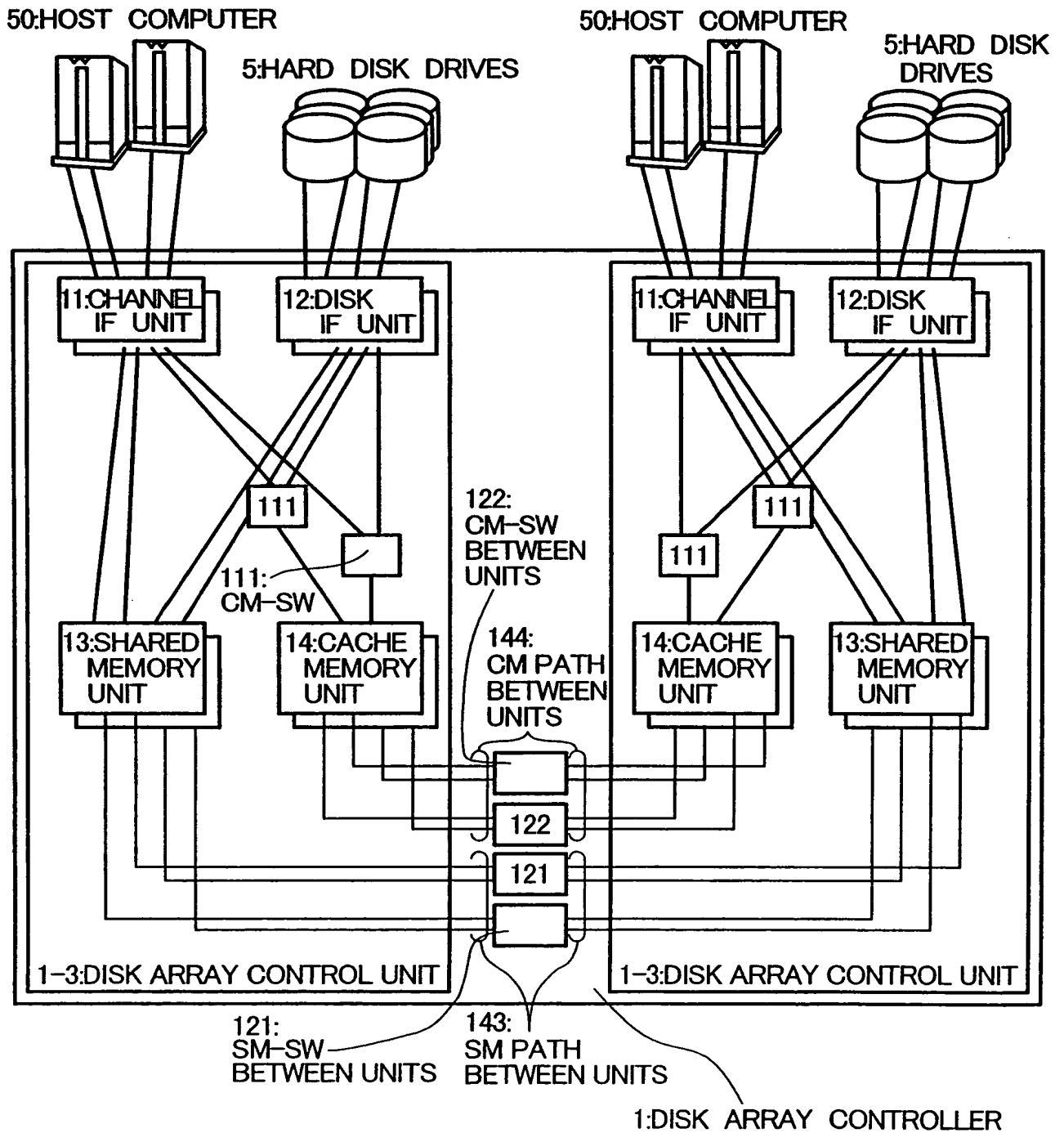


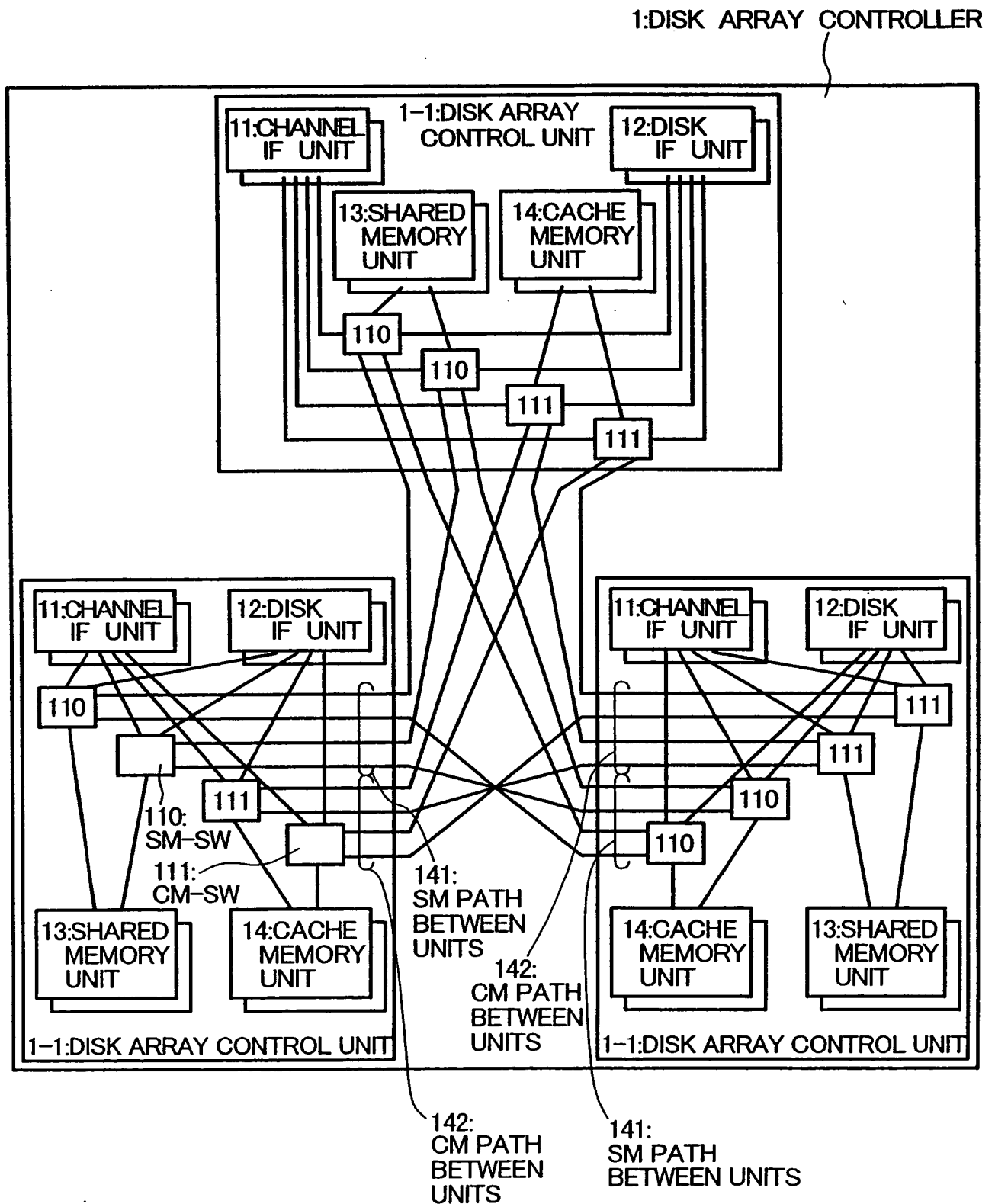
FIG.11



# FIG.12



# FIG.13



# FIG.14

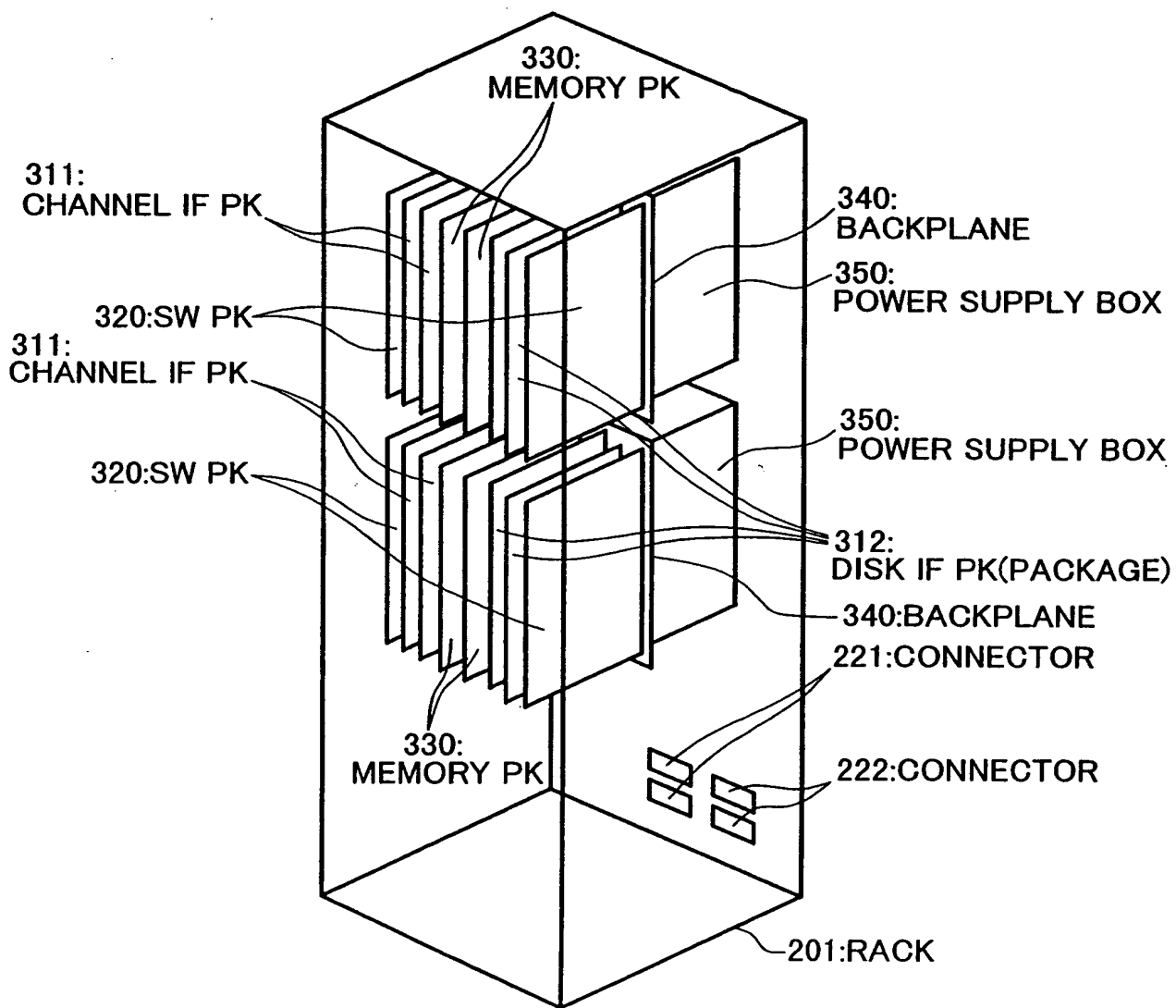
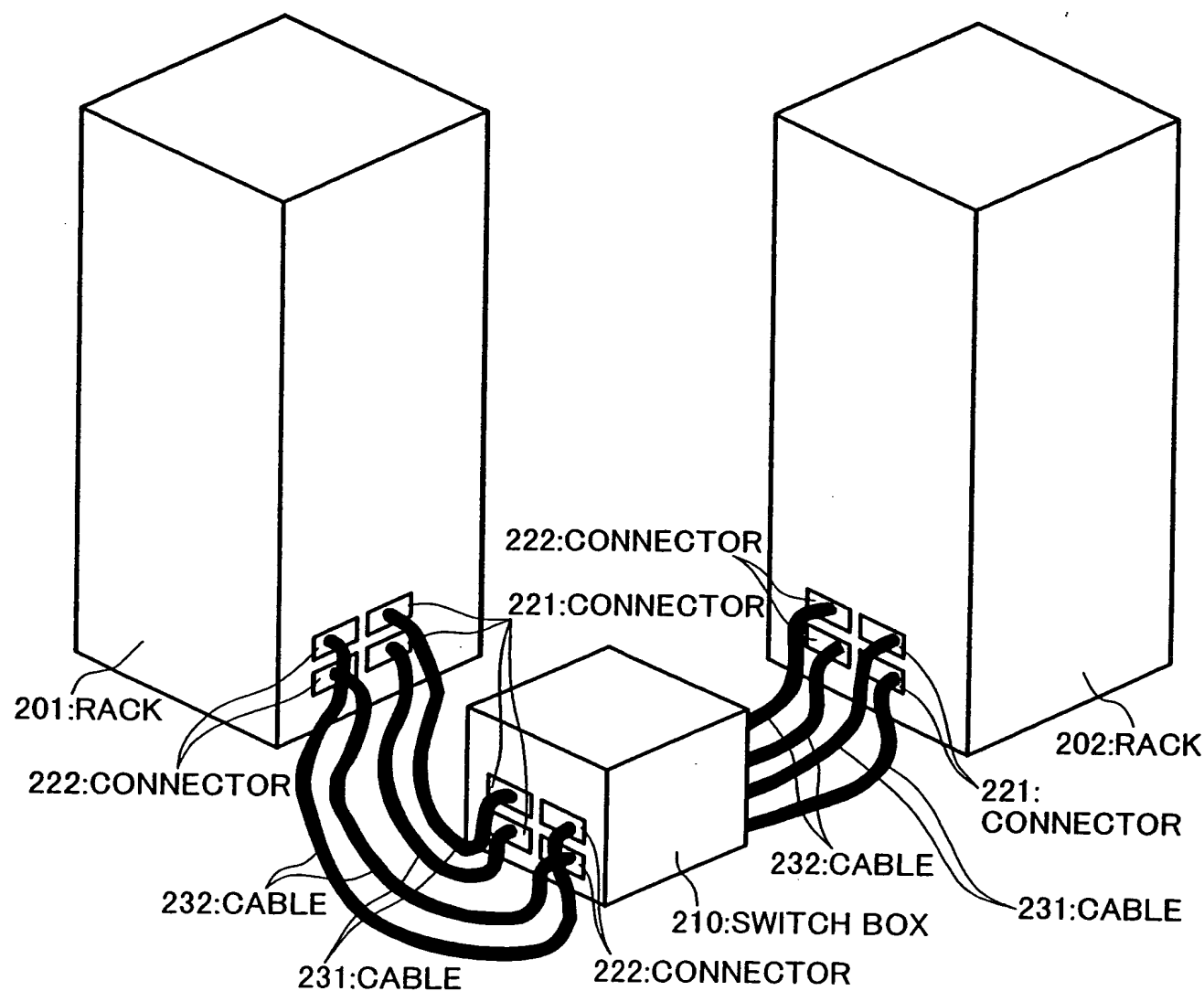


FIG.15



# FIG.16

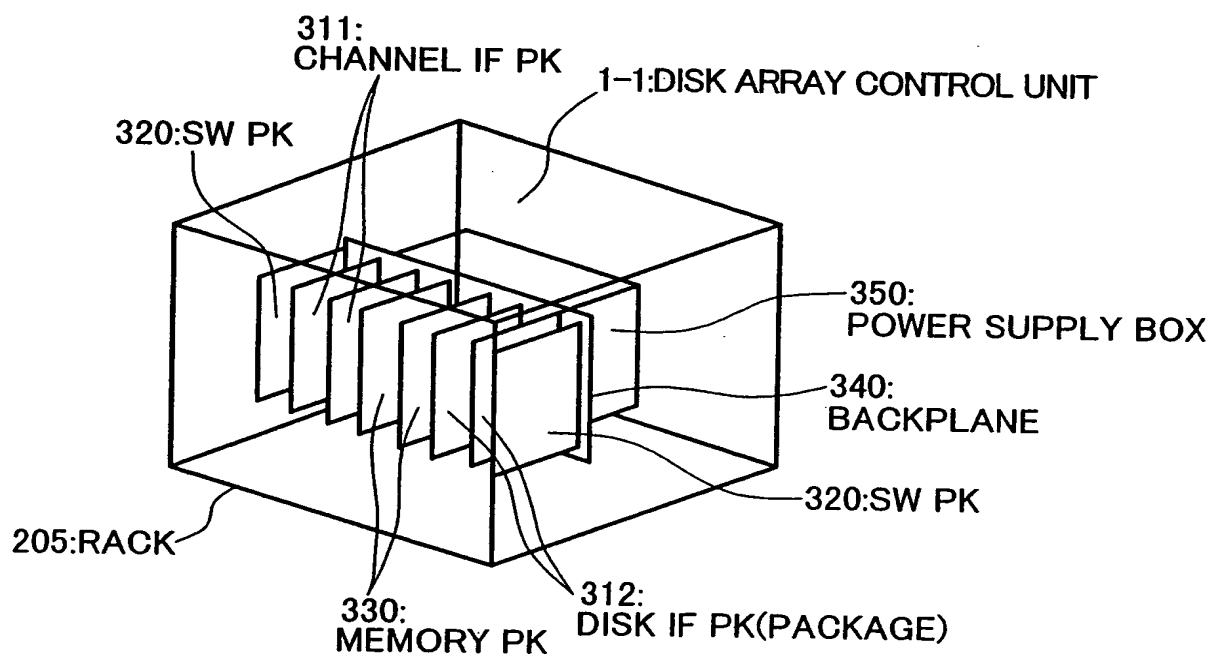
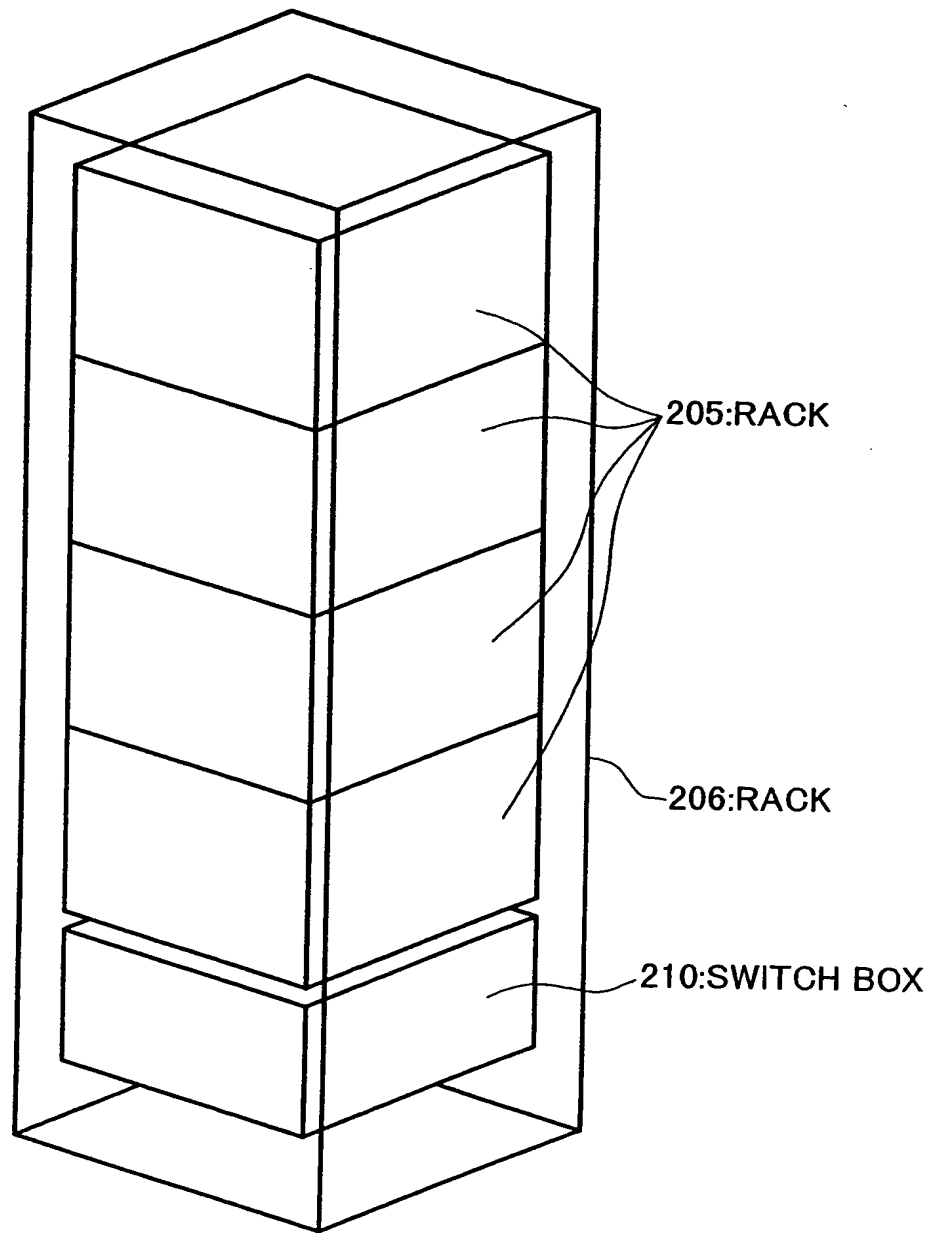


FIG.17



# FIG.18

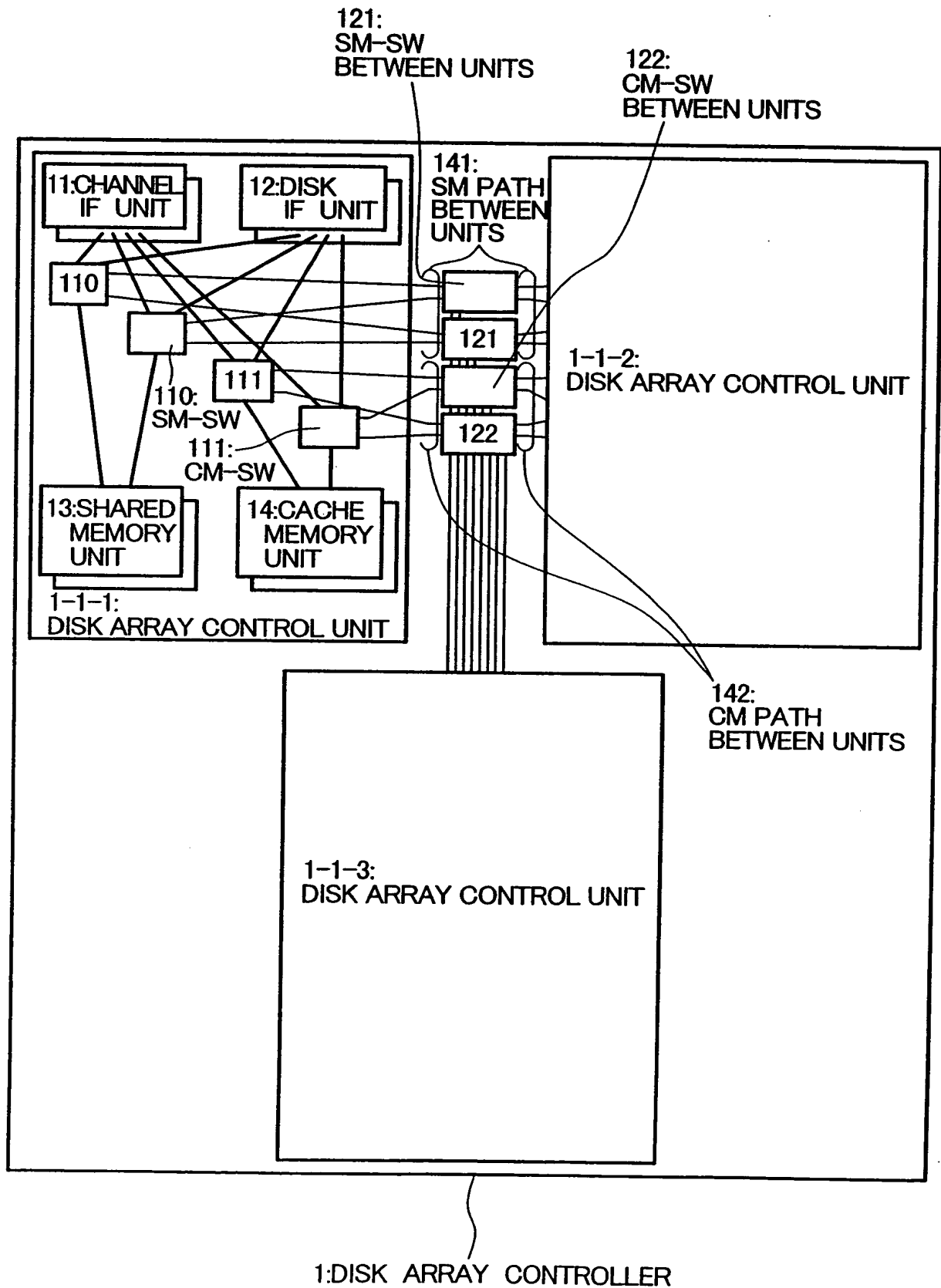
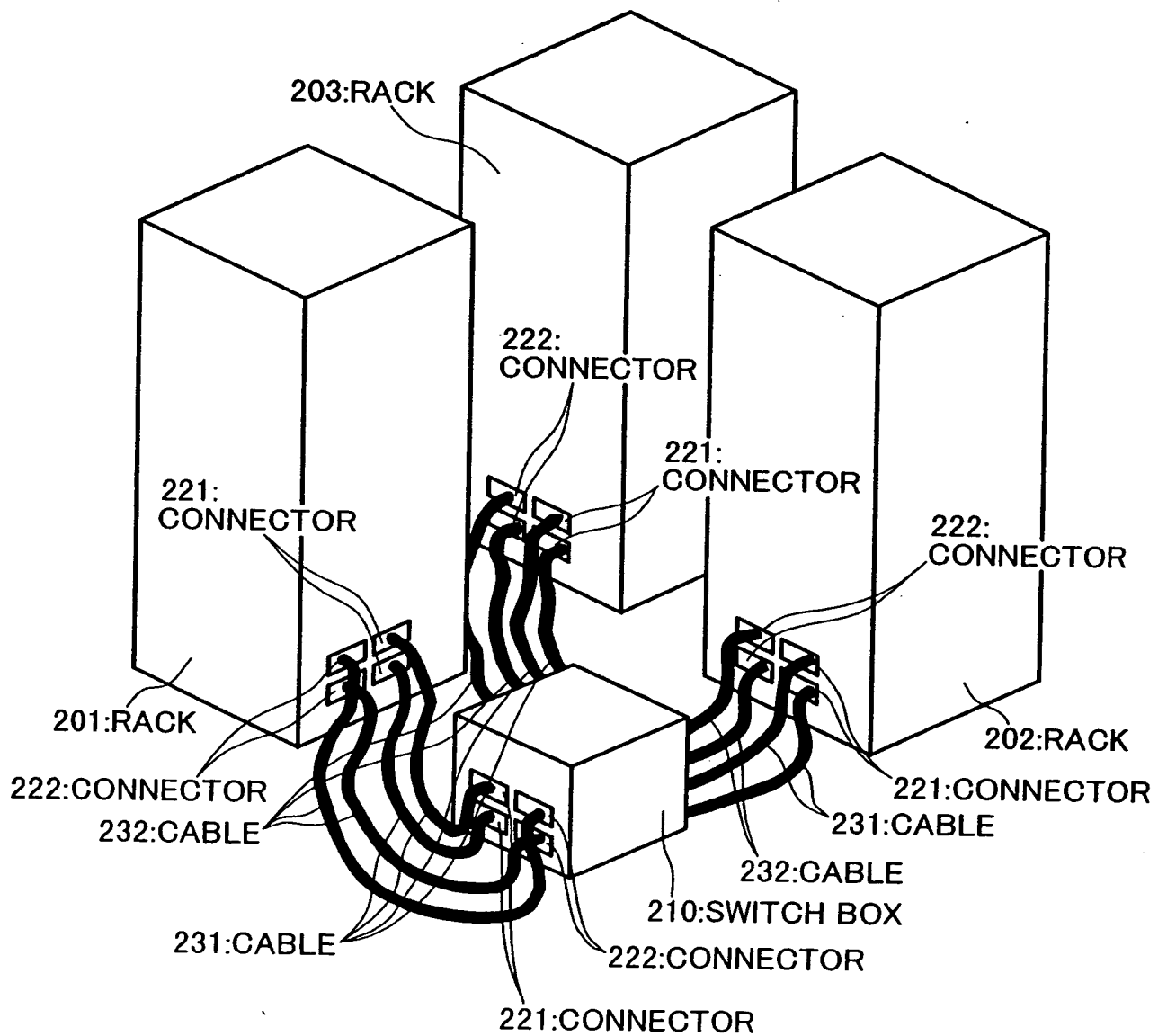
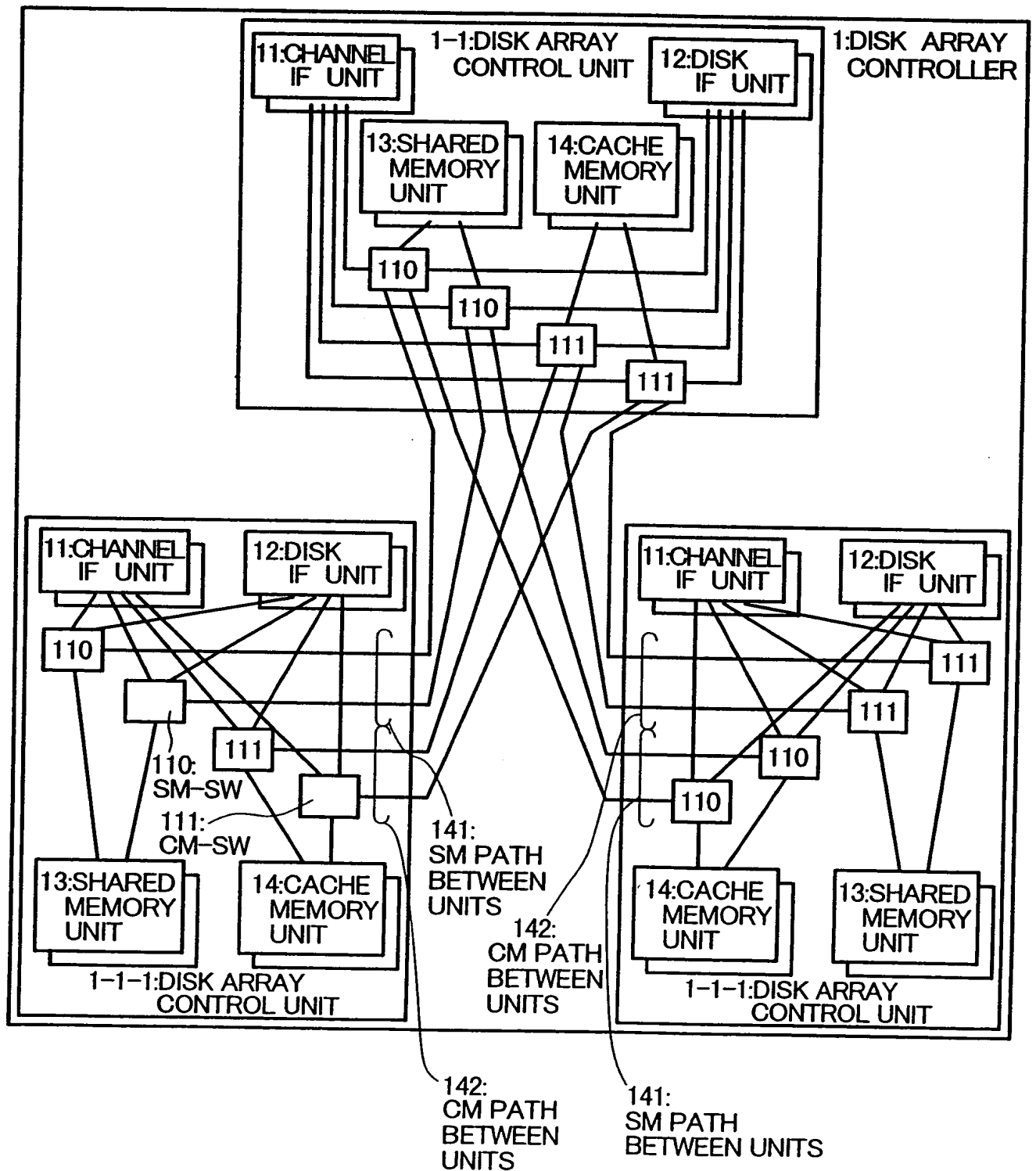


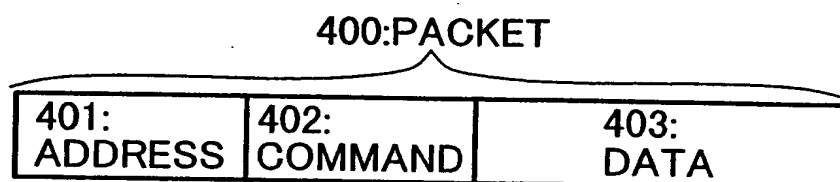
FIG.19



# FIG.20



# FIG.21



# FIG.22

410:REFERENCE TABLE  
FOR PATH SWITCH

ADDRESS.	PORT No.
0 × 0 ··· 000	0
0 × 0 ··· 001	0
⋮	⋮
0 × 0 ··· OFF	0
0 × 0 ··· 100	1
⋮	⋮
0 × 0 ··· 200	2
⋮	⋮